

IN THE SPECIFICATION

Page 7, line 31 to page 8, line 8, paragraph [0039],
replace the paragraph with:

If it becomes necessary to preserve data Dx into EEP-ROM 4 during this process (step S1), the CPU 1 jumps to the leading address of the write control program in the standard program region Ix1, preferably stored in mask ROM 3, by a call ~~call a~~ instruction (step S2). The write control processing of EEP-ROM 4 is executed in accordance with the write control program (step S3). In this manner, a write to EEP-ROM 4 is conducted through the EEP-ROM write control unit 7. During this write operation, the EEP-ROM 4 is electrically isolated from the CPU 1 by a suitable electric switch or gate means, such as a tri-state buffer.